Fort 🗞 ilins, Colorado 80527-2400

10-06-04

PATENT APPLICATION

ATTORNEY DOCKET NO. 10011880-1

IN THE

UNITED STATES PATENT AND TRADEMARK OFFICE

Buswell et al.

Confirmation No.: 2065

PARTICIATION No.: 10/061,492

Examiner: Culbert, R.

Filing Date:

01-31-2002

Group Art Unit: 1763

Title:

Methods and Systems for Forming Slots in a Semiconductor Substrate Containing

Microelectronics

Mail Stop AF **Commissioner for Patents** PO Box 1450 Alexandria, VA 22313-1450

TRANSMITTAL LETTER FOR RESPONSE/AMENDMENT

Sir:							
Tran	nsmitted herewith is/are the following in the	above-identified a	app	olication:			
(X) Response/Amendment		()	Petition to extend time to respond			
()	New fee as calculated below	()	Supplemental Declaration			
(X)	No additional fee						
()	Other:			(fee \$)			
Г	CLAIMS AS AMENDED	BY OTHER THAN A	SM	ALL ENTITY			

(1) FOR			JMBER	BER HIGHEST NUMBER		(5) PRESENT EXTRA		(6) RATE		(7) ADDITIONAL FEES				
TOTAL CLAIMS	i.			м	INUS				=	0	х	\$18	\$	0
INDEP. CLAIMS				М	INUS				=	0	×	\$86	\$	0
[] FIR	ST PR	ESENTATI	ON OF	A ML	JLTIPLE	DEPEN	IDENT	CLAIM			+	\$290	\$	0
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										C	THER	FEES	\$	
						тот	TAL A	DDITIONAL FE	E FOR	THIS A	MEN	OMENT	\$	0

Charge to Deposit Account 08-2025. At any time during the pendency of this 0 application, please charge any fees required or credit any overpayment to Deposit Account 08-2025 pursuant to 37 CFR 1.25. Additionally please charge any fees to Deposit Account 08-2025 under 37 CFR 1.16 through 1.21 inclusive, and any other sections in Title 37 of the Code of Federal Regulations that may regulate fees. A duplicate copy of this sheet is enclosed.

"Express Mail" label no	EV 486243113 US
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Date of Deposit 10-05-2004

I hereby certify that this is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed Commissioner 22313-1450. Patents, Alexandria,

Respectfully submitted,

Paul W. Mitchell

Attorney/Agent for Applicant(s)

Reg. No. 44,453

Date: 10-05-2004

Telephone No.: (509) 324-9256

- Attach as First Page to Transmitted Papers -



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Methods and Systems for Forming Slots in a Semiconductor

Substrate Containing Microelectronics

To:

Commissioner of Patents and Trademarks

PO Box 1450

Alexandria, VA 22313-1450

From:

Hewlett-Packard Development Company, L.P

Intellectual Property Administration

P.O. Box 272400

Fort Collins, Colorado 80527-2400

RESPONSE AFTER FINAL REJECTION

INTRODUCTORY COMMENTS

This communication is in response to the Final Office Action dated 8/05/04, for which a three-month shortened statutory period for response is set for 11/05/04. Applicant submits this communication within a two month time period and respectfully requests that this communication be entered and that a responsive advisory action be generated. Please amend the above-identified application as set forth below.

Amendment to the Claims are reflected on the list of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 4 of this paper.